

COPY OF PAPERS ORIGINALLY FILED

Attorney Docket NSC1-G9800 [P0

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR UNITED STATES PATENT

In re Patent Application of

JEFFREY P. KOTOWSKI, et al.

Application No. 10/067,441

Filed:

February 4, 2002

For:

INTEGRATED CIRCUIT AND METHOD FOR TESTING SAME USING SINGLE PIN TO CONTROL TEST MODE

AND NORMAL MODE OPERATION

Group Art Unit: 2818

Examiner: Unknown

POWER OF ATTORNEY BY ASSENE ESTABLISHMENT OF RIGHT OF ASSISTED TO PROSECUTE UNDER 37 C.F.R. \$3,73(b)

400 Montgomery Street, Suite 1110

San Francisco, CA 94104

(415) 433-2250

Commissioner for Patents Washington, DC 20231

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the above-identified application by assignment dated January 31, 2002 and December 14, 2001, which assignment is recorded in the Patent and Trademark Office at Reel 012573, Frame 0146, hereby appoints the members of the firm of GIRARD & EQUITZ LLP, a firm composed of

Philip A. Girard 28,848

Alfred A. Equitz 30,922

400 Montgomery Street, Suite 1110, San Francisco, CA 94104, (415) 433-2250, FAX (415) 433-2255, and the following members of the National Semiconductor Corporation Intellectual Property Department:

Christopher J. Byrne 32,204

Coleman F. Reif

38,593

Andrew S. Viger

28,552

Eugene C. Conser

39,149

Allen R. Tremain

40,207

Peter Y. Wang

John Maxin

34,668

40,452

as its attorneys/agents with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

GIRARD & EQUITZ LLP Attn: Alfred A. Equitz 400 Montgomery Street. Suite 1110 San Francisco, CA 94104 Telephone: (415) 433-2250

Facsimile: (415) 433-2255

NATIONAL SEMICONDUCTOR CORPORATION

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing

PATENT

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

JEFFREY P. KOTOWSKI, et al.

Application No. 10/067,441

Filed: February 4, 2002

For: INTEGRATED CIRCUIT AND

METHOD FOR TESTING SAME USING SINGLE PIN TO CONTROL TEST MODE AND NORMAL MODE

OPERATION

Commissioner for Patents Washington, D.C. 20231

Group Art Unit: 2818

Examiner: Unknown

TRANSMITTAL OF POWER OF ATTORNEY BY ASSIGNEE

400 Montgomery Street, Suite 1110

San Francisco, CA 94104

(415) 433-2250

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Posta' Service as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on May 10, 2002.

GIRARD & EQUITZ LLP

Date: 05/10/02

Marsha A. Townsend

Sir:

The Power of Attorney by Assignee and Establishment of Right of Assignee to Prosecute Under 37 C.F.R. § 3.73(b) is hereby submitted in the above application and it is respectfully requested that it be made of record.

By:

Respectfully submitted,

GIRARD & EQUITZ LLP

Dated: 5/10/02

Alfred A Equi

Reg. No.30,922

Attorneys for Applicant(s)

Attorney Docket No. NSC1-G9800 [P05051]